

IMS2018 5G-Summit Team Bios



Debabani Choudhury joined Intel Labs in 2006. She provides strategic research directions and leads research and development of RF/millimeter-wave technologies for next generation wireless platforms and connected vehicle integration. She has a broad range of expertise in RF, millimeter wave, and terahertz device, circuits, antennas, system, packaging, integration and technologies. She has 25+ years of research experience and holds a PhD degree in Electrical Engineering. Before joining Intel, she held senior research staff positions at HRL Labs (formerly Hughes Research Laboratories) and Millitech Corporation where she developed various millimeter-wave and terahertz technologies for imaging as well as other space and defense applications. Prior to that, she worked at NASA Jet Propulsion Laboratory (JPL) on THz/submillimeter-wave devices and components for space-based heterodyne receiver applications. Debabani has more than 35 patents/patent applications and numerous publications. She received several NASA recognition awards for her work on heterodyne receivers, devices, multipliers, and guiding structures/ modules developed for space and defense applications. Debabani is an IEEE Fellow. Debabani has been on several Technical Program Committees (TPC) for IEEE and SPIE conferences including IEEE International Microwave Symposium (IMS), IEEE Antenna and Propagation Symposium (AP-S), IEEE Radio and Wireless Symposium (RWS), Wireless Power Transfer Conference (WPTC), European Microwave Conference (EUMC). She serves as member of multiple IEEE Microwave Theory and Technique Society (MTT-S) Technical Co-Ordination Committees: Microwave and Millimeter Wave Integrated Circuits; Wireless Communications; Wireless Energy Transfer and Conversion and Wireless Enabled Automotive and Vehicular Applications. She served on many IMS and RWS conference steering committee roles. She serves as the Guest Editor for multiple journals including Proceedings of IEEE, IEEE Transactions on Microwave Theory and Techniques (T-MTT) and multiple special issues of the IEEE Microwave Magazines. She has presented

numerous invited talks at various conferences, workshops, industries, and universities worldwide. She has presented and organized numerous IEEE workshops, panel sessions, and focused sessions. Currently she is serving as a member of IEEE 5G-Initiative Committee, Chair for MTT-S and ComSoc collaboration team in addition to serving as the Chair for IEEE 5G-Summit, June 2017 and June 2018. She is also serving as a TPC Co-Chair for 5G World Forum 2018.



Dr. Amarpal Khanna is a distinguished engineer at National Instruments in Santa Clara, CA where he is driving innovation in the development of technologies and products for 5G and automotive radar test solutions. His technical expertise and specialties include millimeter-wave transceivers, signal generation, and high-speed technologies up to 100 GHz. He has previously held leadership positions at Phase Matrix, Agilent Technologies, and Hewlett Packard. Dr. Khanna received a B.S. in electronics and communications engineering from PEC Panjab University, India and M.S. and Ph.D degrees from the University of Limoges, France. He has co-authored three books, published more than 50 peer-reviewed journal and conference papers and six patents. Dr. Khanna is an elected member of IEEE MTT ADCOM and was the general chair for the IEEE IMS2016 conference in San Francisco. Dr. Khanna is a Life Fellow of IEEE.



Ashutosh Dutta is currently Senior Wireless Communication Systems Research Scientist at Johns Hopkins University Applied Physics Labs (JHU/APL). Most recently he served as Principal Member of Technical Staff at AT&T Labs in Middletown, New Jersey. His career, spanning more than 30 years, includes Director of Technology Security and Lead Member of Technical Staff at AT&T, CTO of Wireless at a Cybersecurity company NIKSUN, Inc., Senior Scientist in Telcordia Research, Director of Central Research Facility at Columbia University, adjunct faculty at NJIT, and Computer Engineer with TATA Motors. He has more than 90 conference and journal publications, three book chapters, and 30 issued patents. Ashutosh is co-author of the book, titled, "Mobility Protocols and Handover Optimization: Design, Evaluation and Application," published by IEEE and John & Wiley that has recently been translated into Chinese Language. Ashutosh served as the chair for IEEE Princeton / Central Jersey Section, Industry Relation Chair for Region 1 and MGA, Pre-University Coordinator for IEEE MGA and vice chair of Education Society Chapter of PCJS. He co-founded the IEEE STEM conference (ISEC) and helped to implement EPICS (Engineering Projects in Community Service) projects in several high schools. Ashutosh currently serves as the Director of Industry Outreach for IEEE Communications Society and is the founding co-chair for IEEE 5G initiative. He also serves as IEEE Communications Society's Distinguished Lecturer for 2017-2018. He was recipient of the prestigious 2009 IEEE MGA Leadership award and 2010 IEEE-USA professional leadership award. Ashutosh obtained his BS in Electrical Engineering from NIT Rourkela, India, MS in Computer Science from NJIT, and Ph.D. in Electrical Engineering from Columbia University under the supervision of Prof. Henning Schulzrinne. Ashutosh is a senior member of IEEE and ACM.



Janet O'Neil is a customer relations specialist with ETS-Lindgren, located in Los Angeles, California. She has over 30 years of experience in the RF Microwave and Electromagnetic Compatibility (EMC) industries. She is a member of the Board of Directors of the IEEE Electromagnetic Compatibility (EMC) Society and is a member of Subcommittee 1 (Techniques and Development) of ANSI ASC C63®. She was chair of the 2007 IEEE International Symposium on EMC in Honolulu, Hawaii and Publications Chair for the IEEE International Microwave Symposium (IMS) 2013 in Seattle, Washington. Currently she serves as vice-chair of the IEEE Coastal Los Angeles EMC Chapter and as vice-chair of the 2018 IEEE Symposium on EMC and Signal/Power Integrity in Long Beach, California. Her education includes BA degrees in English and in Business Economics from the University of California, Santa Barbara. She may be reached at janet.oneil@ets-lindgren.com.



Dylan F. Williams received a Ph.D. in Electrical Engineering from the University of California, Berkeley in 1986. He joined the Electromagnetic Fields Division of the National Institute of Standards and Technology in 1989 where he develops electrical waveform and microwave metrology. He has published over 100 technical papers, is a Fellow of the IEEE, is the recipient of the 2013 IEEE

Joseph F. Keithley Award. He served as Editor of the IEEE Transactions on Microwave Theory and Techniques and as 2017 President of the IEEE Microwave Theory and Techniques Society.



Doug Zuckerman is a longstanding active IEEE volunteer leader. He serves on the IEEE Future Directions Committee and IEEE Big Data steering committee, represents IEEE on the OpenFog Consortium Board of Directors, is a past IEEE Communications Society president, and has served on ComSoc's Board of Governors and the IEEE Board of Directors. He received BS, MS and PhD degrees from Columbia University and is an IEEE Life Fellow. His career at Bell Labs and Telcordia Technologies spans the operations, management and engineering of emerging communications technologies, networks and applications. His work heavily influenced standards for management of telecommunications networks. Currently, he is a Consultant for Vencore Labs (Applied Communications Sciences).



Stefano Pellerano is a principal engineer at Intel Labs, leading the Next Generation Radio Integration Lab. He drives several research activities

focused at enabling radio circuit integration in deeply-scaled CMOS technologies. Since joining Intel Labs in 2004, Stefano worked on MIMO transceivers for WiFi, digital PLLs, high-efficient digital architectures for polar and outphasing transmitters, mm-wave radio transceiver and phased-array systems, and low-power radios. His latest research interests also include cryogenic CMOS integrated electronics for qubit control in fault tolerant scalable quantum computers. Before joining Intel, Stefano got his Laurea and PhD degrees from Politecnico di Milano, Italy, in 2000 and 2004 respectively, after an internship in 2003 with Agere System (former Bell Labs) in Allentown, PA. His PhD thesis focused on the design of fully-integrated low-power frequency synthesizers for WLAN applications.